



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of: Timothy E. Fiscus et al.
Serial No.: 10/629,667
Title: METHOD AND ARCHITECTURE FOR REDUCING THE
POWER CONSUMPTION FOR MEMORY DEVICES IN
REFRESH OPERATIONS
Filed: July 29, 2003
Attorney Docket No.: 0325.00519c
Examiner: Mai, S.
Art Unit: 2818
In Response To: Office Action mailed July 20, 2004

CERTIFICATE OF MAILING

I hereby certify that this letter, the response or amendment attached hereto are being deposited with the United States Postal Service as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on September 17, 2004.

By:

Jan M. Dunbar
Jan M. Dunbar

AMENDMENT AFTER FINAL

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Office Action mailed July 20, 2004
please amend the above-identified application as follows:

09/21/2004 CNGUYEN 00000026 10629667

01 FC:1201

86.00 OP

Entry is denied
sk 9/24/04